

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	11114	(semiconductor\$1 or integrated adj circuit\$1) with (switch\$5 and (resistor\$1 or resitance\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/21 13:26
L2	2196	1 and test\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/21 13:26
L3	1994	2 and control\$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/21 13:27
L4	335	3 and (terminals same (input\$4 adj output\$4 or i/o\$1))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/21 13:28
L5	9	("3668551" "3864817" "4998180" "5670824" "5751555" "6020636" "6078514" "6369662" "6424034").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/07/21 14:02